



NOTE 10

ASI2416 RevC Errata

10/25/05

This errata applies to the following:

- ASI2416 revC Modular Cobranet Interface running v0.18 firmware.
- ASI1431 revA Analog Module
- ASI1441 revA AES/EBU Module
- ASI1451 revA GPIO Module

1. ASI1431 Analog Module.

1.1 The analog I/O only works in un-balanced mode. All the -ve input and output audio signals are connected to GND. This causes the I/O signals to be 6dB less than set with the level control and the signal quality does not meet spec.

2. ASI1441 AES/EBU Module.

2.1 The Voltage swing on all AES/EBU I/O is half what it should be. This should not affect most applications.

2.2 The AES/EBU outputs have the left and right channels swapped. The inputs are correct.

2.3 AES/EBU clock source selection on digital outputs is not working yet. Outputs will always be clocked at 48kHz.

3. ASI1451 GPIO Module.

3.1 GPIO is implemented using the "old" HPI function call interface as used with the ASI4336/4346. Window messages will be generated. Performance is less than ideal.

3.2 Only the following GPIO signals are available and the pinout does not match the label on the ASI1493 Terminal Block Module Connector:

OPTA1, OPTA4, OPTB1, OPTB4, OPTC1, OPTC4, OPTD1, VOPT, GND
 RLYA1, RLYA4, COMA, RLYB1, RLYB4, RLYC1, RLYC4, RLYD1, RLYD4.

The pinout is:

| Terminal Block 1 | | | | | Terminal Block 2 | | | | | Terminal Block 3 | | | | | Terminal Block 4 | | | | |
|------------------|------|---|-------|---|------------------|---|---|-------|---|------------------|---|---|-------|---|------------------|---|------|-------|-----|
| 1 | 2 | 3 | 4 | 5 | 1 | 2 | 3 | 4 | 5 | 1 | 2 | 3 | 4 | 5 | 1 | 2 | 3 | 4 | 5 |
| RLYA1 | COMA | | RLYA4 | | RLYB1 | | | RLYB4 | | RLYC1 | | | RLYC4 | | RLYD1 | | | RLYD4 | GND |
| OPTA1 | | | OPTA4 | | OPTB1 | | | OPTB4 | | OPTC1 | | | OPTC4 | | OPTD1 | | VOPT | | |
| 1 | 2 | 3 | 4 | 5 | 1 | 2 | 3 | 4 | 5 | 1 | 2 | 3 | 4 | 5 | 1 | 2 | 3 | 4 | 5 |
| Terminal Block 5 | | | | | Terminal Block 6 | | | | | Terminal Block 7 | | | | | Terminal Block 8 | | | | |

<end>